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Abstract:

A phase-locked loop circuit comprises a voltage-controlled oscillator with at least one resonator circuit for driving the oscillator to an output frequency which is a multiple of the resonator frequency. The voltage-controlled oscillator is connected to a phase-locked loop comprising frequency control means for controlling the output frequency of the oscillator. According to the invention the resonator circuit comprises at least one adjustable component and the frequency control means are coupled into the resonator circuit for controlling the resonator frequency of the resonator circuit. The invention moreover relates to a voltage-controlled oscillator as used in this phase-locked loop circuit.